



Program Schedule– 28 March 2025:

Admin Block, Main Seminar Hall

Activity	Time
Registration / Morning Networking Tea	08:30 - 09:15
HODs, Professors, Faculty, Student to be Seated	09:15 - 09:25
Arrival of Delegates	09:25 - 09:30
Lamp Lighting	09:30 - 09:35
Sapling Presentation to the Dignitaries	09:35 - 09:45
Welcome Speech by the Director General	09:45 - 10:00
Introduction to the conference theme and the entire program by the General Chair, Dr. Rahul Sharma	10:00 - 10:20
Address by Prof. (Dr.) Dirk Draheim, General Chair NGISE	10:20 - 10:30
Address by the Guest of Honor, Prof. Satish Kumar Singh	10:30 - 11:00
Address by the Chief Guest, Prof. (Dr.) Chuan-Yu Chang	11:00 - 11:15
Release of Proceedings	11:15 - 11:20
Presentation of Memento	11:20 - 11:30
High Tea	11:30 - 12:00
Keynote Address I: Salvatore Sinno, VP of Innovation, Unisys, USA	12:00 - 12:45
Keynote Address II: Prof. Chuan-Yu Chang, Professor, National Yunlin University of Science and Technology, Taiwan	12:45 - 13:30
Lunch	13:30 - 14:30
Technical Sessions 1 to 5 Venue-Main seminar hall, Mini seminar hall-1, Mini seminar hall-2, Lab 6 and Lab 7	14:30 - 17:00
Evening Tea	17:00 - 17:20



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Activity	Time
Morning Networking Tea	08:45 - 09:15
Faculty and participants to be Seated	09:15 - 09:30
Keynote Address III: Pavan Kumar Adepur, GenAI/LLM Research Leader, Head of Engineering, Amazon.com LLC, USA	09:30 - 10:15
Panel Discussion	10:15 - 11:00
High Tea	11:00 - 11:30
Visit to the Centres of Excellence	11:30 - 13:00
Lunch	13:00 - 13:45
HODs, Professors, Faculty, Student to be Seated	13:45 - 13:50
Arrival of Delegates	13:50 - 13:55
Sapling Presentation to the Dignitaries	13:55 - 14:00
Address by the Chief Guest, Prof. Sri Niwas Singh	14:00 - 14:30
Conference report presentation	14:30 - 14:45
Certificate Distribution	14:45 - 15:05
Presentation of Memento	15:05 - 15:10
Vote of Thanks by the General Chair, Dr. Rahul Sharma	15:10 - 15:15
Evening Tea	15:15 - 15:45

Parallel Activities – 29 March 2025

Activity	Time
Technical Sessions 6-10 Venue- Admin Block-Mini seminar hall-1, Mini seminar hall-2 CSIT Block-Lab-6 (5 th floor), Lab-7 (5 th floor), CSIT seminar hall (1 st floor)	08:30 - 11:30
Technical Sessions 11-15 Venue- Admin Block-Mini seminar hall-1, Mini seminar hall-2 CSIT Block-Lab-6 (5 th floor), Lab-7 (5 th floor), CSIT seminar hall (1 st floor)	11:30 - 01:30
IEEE WIE Meet and Greet Program Admin block-DG Conference Hall	11:30 - 12:30
IEEE Computational Intelligence Student Chapter Activity Poster Presentation. Admin Block- Outside Main Seminar Hall	11:30 - 12:30